

First Named Inventor	Frankie Roohparvar	<p style="text-align: center;"><b>INFORMATION DISCLOSURE STATEMENT</b></p>
Serial No.	Unknown	
Filing Date	Filed herewith	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Attorney Docket No.	400.094US03	
<p>Title: METHOD AND ARCHITECTURE TO CALIBRATE READ OPERATIONS IN SYNCHRONOUS FLASH MEMORY</p>		

Mail Stop: Patent Application  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450


In compliance with 37 C.F.R. §§ 1.56 and 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application. Applicant respectfully requests that this Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

Further pursuant to MPEP §609, because all of the references listed on the attached Form 1449 have been previously submitted and made of record in the parent application, U.S. Patent Application 10/017,892, filed December 12, 2001, copies of the references previously made of record in the parent application are not submitted herewith.

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at the number listed below.

Respectfully submitted,

Date: 2/27/04

  
Andrew C. Walseth  
Reg. No. 43,234

Attorneys for Applicant  
Leffert Jay & Polglaze, P.A.  
P.O. Box 581009  
Minneapolis, MN 55458-1009

T – 612/312-2200  
F – 612/312-2250

First Named Inventor	Frankie Roohparvar	<b>INFORMATION DISCLOSURE STATEMENT FORM PTO-1449</b>
Serial No.	Unknown	
Filing Date	Herewith	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Attorney Docket No.	400.094US03	
Title: METHOD AND ARCHITECTURE TO CALIBRATE READ OPERATIONS IN SYNCHRONOUS FLASH MEMORY		

U.S. Patent References				
Examiner Initials	Publication/Patent No.	Publication/Issue Date	Name	Filing Date
	5,696,917	12-09-1997	Mills et al	06-03-1994
	5,847,994	12-08-1998	Motoshima et al.	03-24-1998
	6,026,465	02-15-2000	Mills et al	06-18-1997
	6,104,667	08-15-2000	Akaogi	07-30-1999
	6,104,668	08-15-2000	Lee et al	08-15-2000
	6,185,128B1	02-06-2001	Chen et al.	10-19-1999
	6,208,564B1	03-27-2001	Yamada et al	10-29-1999
	6,246,609B1	06-12-2001	Akaogi	07-30-1999
	6,246,626B1	06-12-2001	Roohparvar	07-28-2000
	6,266,282B1	07-24-2001	Hwang et al	08-12-1999
	6,275,446B1	08-14-2001	Abedifard	08-25-2000
	6,278,654B1	08-21-2001	Roohparvar	06-30-2000
	6,304,488B1	10-16-2001	Abedifard et al	08-25-2000
	6,304,497B1	10-16-2001	Roohparvar	06-30-2000
	6,304,510B1	10-16-2001	Nobunaga et al	08-31-2000
	6,307,790B1	10-23-2001	Roohparvar et al	08-30-2000
	6,307,779B1	10-23-2001	Roohparvar	07-28-2000
	6,314,049B1	11-06-2001	Roohparvar	05-11-2000
	6,515,900B2	02-04-2003	Kato et al.	04-12-2001
	6,552,934B2	04-22-2003	Roohparvar	02-26-2002

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	T?
	Country	No.			

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.

Examiner Signature		Date Considered	
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			